

Actively Multiplexed μ ECoG Array Based on Thin-Film Electronics for High-Resolution Brain Mapping

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Introduction: Electrode arrays are used in neuroscience research and the clinic to record electrical activity from the surface of the brain. However, current passive electrocorticography (ECoG) technologies have low spatial resolution and/or limited cortex coverage. The electrode-count and density are restricted by the fact that each electrode must be individually wired. Here, we present an active μ ECoG that circumvents these challenges while achieving significantly lower noise compared to other existing active μ ECoG arrays.

Material, Methods and Results: The proposed neural interface consists of a flexible, actively-multiplexed 256-electrode array and an incremental- $\Delta\Sigma$ readout integrated circuit (ROIC) [1, 2]. The 1x1-cm² μ ECoG array is based on a metaloxide thin-film transistor technology on a 15- μ m flexible foil, Fig.1(a), and is coupled to a 1.25x1.25-mm² CMOS ROIC fabricated in a 22-nm FDSOI process. Thanks to the 256:16 time-division multiplexing achieved in the electrode array, only 16 multiplexed channels are required to acquire signals from all the 256 electrodes simultaneously. The proposed system has been validated in-vivo by recording spontaneous activity and somatosensory-evoked potentials in anesthetized mice, Fig.1(b).

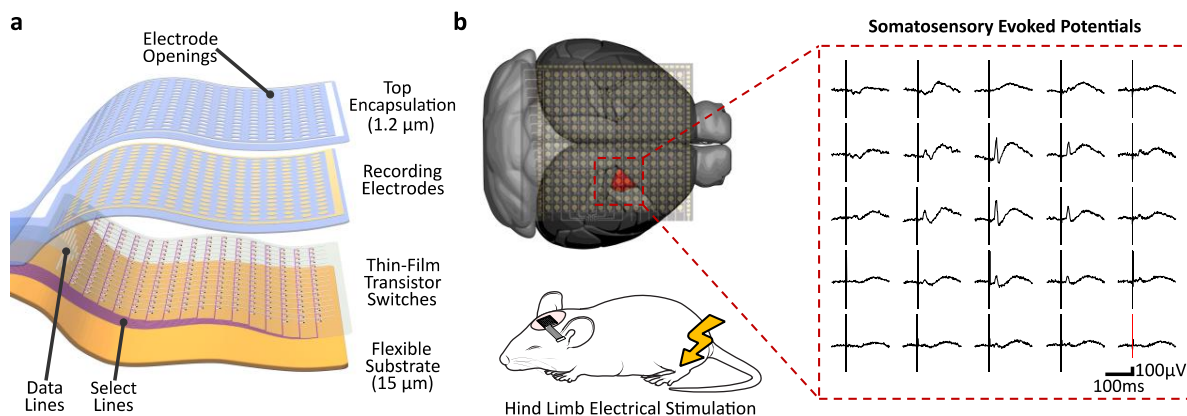


Figure 1. Actively multiplexed μ ECoG array based on metaloxide thin-film transistors. a) Exploded-view illustration of the 256-electrode array, b) in vivo validation by recording somatosensory-evoked potentials.

Discussion: By combining TFT multiplexing with newly proposed bulk-DAC feedback in the readout channel, we can integrate and address more electrodes than other passive arrays, achieve >10x less noise than existing active arrays, and obtain >2x effective channel area reduction in the ROIC, while maintaining comparable electrical performance over state-of-the-art ECoG readouts.

Significance: Our neuroelectronic interface platform overcomes the wiring bottle neck that limits many neural acquisition systems and has application potential as a tool for better mapping the cerebral cortex or as an enabling technology of future brain-machine interfaces.

References

- [1] Huang, X., Londoño-Ramírez, H., Ballini, M., Van Hoof, C., Genoe, J., Haesler, S., Gielen, G., Van Helleputte, N., Lopez, C.M.. A 256-Channel Actively-Multiplexed μ ECoG Implant with Column-Parallel Incremental $\Delta\Sigma$ ADCs Employing Bulk-DACs in 22-nm FDSOI Technology. In *2022 IEEE International Solid-State Circuits Conference (ISSCC)*, Vol. 65, 200-202, 2022.
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