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INFORMATION, COMMUNICATION & COMPUTING

Fields of Expertise TU Graz



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The measures regarding the novel Coronavirus have forced a shift in human life from the real world to the virtual world and the internet. This triggered an unprecedented push to the adoption of digitalization: school and university classes are taught online, cultural institutions stream programs over the internet, meetings are held online, decision-making bodies such as our Senate are adopting their regulations to enable easier online decisionmaking, smartphone tracking and big data analytics are being considered or already used to monitor human activity and compliance to lockdown rules, digital fabrication of parts for low-cost ventilators should improve medical treatment, robotics is used to speed up processing of COVID-19 tests in the lab. I am curious about how much of this is here to stay after the return to a normal (physical) life and how it will change our lives also in the long run.

Electronics-based systems – the topic of this edition of TU Graz research – form

one foundation for this digitalization and Graz is turning into an international hotspot in this regard with the establishment of the Silicon Austria Labs (SAL) on the campus of TU Graz. Recently, collaboration formats between SAL and the University have kicked off, including the establishment of two so-called TU Graz-SAL Research Labs where SAL researchers and TU Graz researchers are jointly investigating basic research questions in two areas: Dependable Embedded Systems and Electromagnetic Compatibility. Bernhard Aichernig (Computer Science) and Bernd Deutschmann (Electrical Engineering) report about these topics in this edition of TU Graz research. Enjoy reading!

Bernd Deutschmann:

Electromagnetic Compatibility of Electronics Based Systems Affects Us All

In recent decades, semiconductor technologies have been shrinking, while the operating frequencies and integration density of modern integrated circuits (ICs) have increased significantly. As a result, the electromagnetic emission (EME) of modern ICs and their electromagnetic susceptibility (EMS) have become a major issue in the design of electronics-based systems (EBS). In this context, engineers are increasingly required to ensure that their developed products function satisfactorily in their electromagnetic environment, i.e. that they are not functionally affected by electromagnetic interferences, but also that they do not negatively influence other devices through their electromagnetic emission.

But before we get into that, we should think about why electromagnetic compatibility of electronic devices and systems concerns us all and why this topic should be important for everyone. If the video that we are watching on our tablet gets stuck due to an electrostatic discharge that occurs as our fingers approach the tablet, it is annoying in most cases, but usually not life-threatening.

However, the situation might be different when electromagnetic interference occurs during the operation of avionics,



Figure 1: Example of a DPI test bench used to characterize the susceptibility of an operational amplifier.

Source: Andrea Lavarda [3]





Figure 2: Surface scanning system at the Institute of Electronics. Source: Bernd Deutschmann, Institute of Electronics - TU Graz

automotive or industrial applications. Here, interference can very often have life-threatening consequences.

An example from the automotive sector should illustrate this. Airbag systems are designed to protect passengers in the event of an accident. The heart of the airbag system is the so-called airbag control unit (ACU), which usually consists of an "application-specific integrated circuit" or ASIC that monitors the signals of the crash sensors in the vehicle to determine whether the airbags need to be triggered or not. The prerequisite, however, is that the ASIC is not functionally influenced by electromagnetic interferences (EMI). Ensuring the EMC and at the same time the functional safety of modern airbag systems under all circumstances is difficult in many cases, as different types of interference can occur and a complete test of the ACU at all levels, i.e. from AISC, the control module to the finished vehicle, is usually very extensive.

Automakers and airbag suppliers have known about such kinds of interferences and the consequent malfunctioning units that caused airbags in certain vehicles to fail to deploy and have left passengers without expected protection during a crash for many years. But measures are usually not taken until years later, when the first fatalities associated with the failure of electronic systems have occurred.

The most widespread EMI problem in

the media started in 2016 when many automakers began recalling millions of cars for airbag malfunctions [1]. The National Highway Traffic Safety Administration (NHTSA) announced at the time that this airbag problem involved six different car companies and affected an estimated 12.3 million vehicles. But this was by no means the largest airbag recall operation in recent years [2].

The demand for robust electronic systems capable of operating satisfactorily in their electromagnetic environment is increasing, and for safe operation it is very important that all designed functions work properly and sensed signals are reliable and accurate. Many EMC problems of electronic systems can be attributed to interference issues caused by the low immunity of the ICs that are used. Therefore it is important to solve EMC problems directly at their source.

ENSURING EMC

Well, one of the approaches is that great attention must be paid to EMC behavior during the design of the particular IC. Especially with the increasing density of components in modern ICs, their emission and immunity performance has become a design factor to be considered.

DIRECT POWER INJECTION

CMOS operational amplifiers that are built into automotive systems such as smart power switches are very often used as sensor interfaces as part of their implemented electronic sensor systems. Often their power supply and inputs are directly connected to the wiring harness and are therefore particularly susceptible to any kind of interference received by the cables that can act as receiving antennas. As a consequence operational amplifiers can be heavily disturbed and must therefore withstand a high level of electromagnetic interference [3].

Figure 1 shows a block diagram of the EMC test bench that can be used to characterize the susceptibility of operational amplifiers in the EMC lab of the Institute of Electronics. The interference signal is fed into one of the inputs of the operational amplifier at wafer level by means of special feed heads (GSG probe) via a radio-frequency (RF) generator. The bias T is used to decouple the DC path (VI_{N nom}) from the AC path (V_{emift}), while the 20dB attenuator counteracts the reflections that occur along the injection path due to impedance mismatch. The ammeter monitors the bias current (I_{bias}) used to bias the amplifier. The robustness of the operational amplifier is monitored by a multimeter which measures and averages the output voltage $V_{\mbox{\tiny OUT}}$ that should not change due to EMI. During characterization, the forward power of the interference signal is gradually increased at the start frequency (e.g. 1MHz) until the monitored output voltage >

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Figure 3: Surface scan of the magnetic near field of an IC (white/ red shows high, while blue/purple low magnetic fields). Source: Bernd Deutschmann,

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exceeds a predefined limit. Then the frequency of the interference signal is increased by one step and the procedure is started again. This is done until the stop frequency is reached (e.g. 1GHz). The resulting diagram is called DPIcharacteristic of the operational amplifiers input over the frequency range of interest. If this characteristic is above a given limit, the operational amplifier is classified as robust; otherwise the designer is given some hints as to the frequency range in which the circuit needs to be improved.

SURFACE SCAN

Manv different techniques are currently available to characterize the electromagnetic emission of ICs. With the so-called surface scan method that is described in the international standard IEC 61967-3, IC designers have a very useful characterization method at their disposal in order to find the sources of the electromagnetic emission directly on the surface of their ICs [4], [5]. This method is based on the measurement of electric and magnetic near-fields at the surface of IC packages or microchips. During measurement the device under test (DUT) is powered up and fully operational while the generated near-field emission spectrum is measured by automatically moving a near-field probe over the DUT. This technique enables the distribution to be visualized over a wide frequency range of near-fields emitting from the surface of the DUT.

Figure 2 shows the surface scan system of the Institute of Electronics which consists of a micromanipulation (wafer prober)



that is equipped with stepper motors to control the movement of the DUT in relation to a specially designed magnetic near-filed probe that is connected via an amplifier to a spectrum analyzer.

The magnetic near-fields at the surface of an IC under test are shown in Figure 3 in a color-coded representation that is superimposed on the photo of the microchip. This picture shows different areas with higher (white, red) and lower magnetic fields (blue, violet) and gives the IC designers a good overview of the locations of the emission sources and thereby helps to identify and analyze emission hotspots. In addition, an augmented reality app running on a smartphone or tablet can be used to superimpose the corresponding measurement results semi-transparently on a DUT and thus allows the observation of the field distributions from different directions. Figure 4 shows an example of such an augmented reality application.

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- [2] Consumer Reports, "Takata Airbag Recall -Everything You Need to Know", 2018, <u>https://</u> www.consumerreports.org/cro/news/2016/05/

Figure 4: Visualization of the near-fields by using an augmented reality app. Image source: Philipp Reitter

Source: Philipp Reitter, Andreas Gleinser, Bernd Deutschmann, Institute of Electronics – TU Graz

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Bernd Deutschmann is professor at the Institute of Electronics. His research interests include the EMC aware design of electronic systems and integrated circuits.

Source: Institute of Electronics - TU Graz