

## Influence of process parameters on the properties of TEOS DF-PECVD grown SiO<sub>x</sub> films by DOE

E. Mikmeková<sup>1</sup>, J. Janča<sup>2</sup>, M. Dvořáková<sup>1</sup>

1. Institute of Scientific Instruments ASCR, v.v.i., Královopolská 147, 61264 Brno, Czech Republic
2. Institute of Physical Electronics, Faculty of Science, Masaryk University, Kotlářská 2, 611 37 Brno, Czech Republic

eliska@isibrno.cz

Keywords: DF-PECVD, Silicon dioxide, Intrinsic stress, DOE, SEM

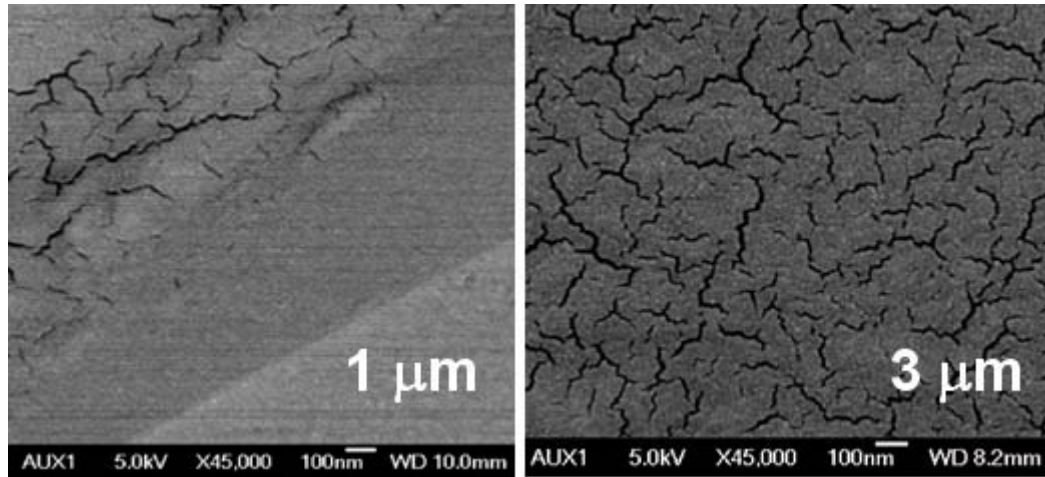
The aim of this work is to optimize deposition process for thin silicon dioxide films preparation and to study the influence of stress in these films. SiO<sub>x</sub> films have been deposited by DF-PECVD (dual frequency plasma enhanced chemical vapor deposition) method in the Applied Materials P5000 single wafer tool. The main purpose of using the high frequency is to generate reactive species and to provide sufficient electron and ion densities. Low frequency is used to control plasma sheath voltage (by controlling the ion bombardment it is possible to eliminate intrinsic stress) [2]. This reactor is a multichamber vacuum load lock deposition system. DF-PECVD (13.56 MHz and 350 kHz) process uses a mixture of TEOS (tetraethoxysilane) and O<sub>2</sub> as a reactive gas to deposit thin SiO<sub>x</sub> films. For better uniformity of films, the upper electrode had a showerhead design.

Silicon wafers (150 mm, 525 mm thickness, (100) orientation, n-type, borone doped) have been used as a substrate. Susceptor temperature has been held on the constant temperature of 400°C (~ 357°C on silicon wafer) by the use of infrared (IR) lamps. Chamber pressure during deposition process was 4.8 torr and temperature of bubbler 39 °C. The power of high and low frequency, inter-electrode spacing and gas flow of TEOS has been changed.

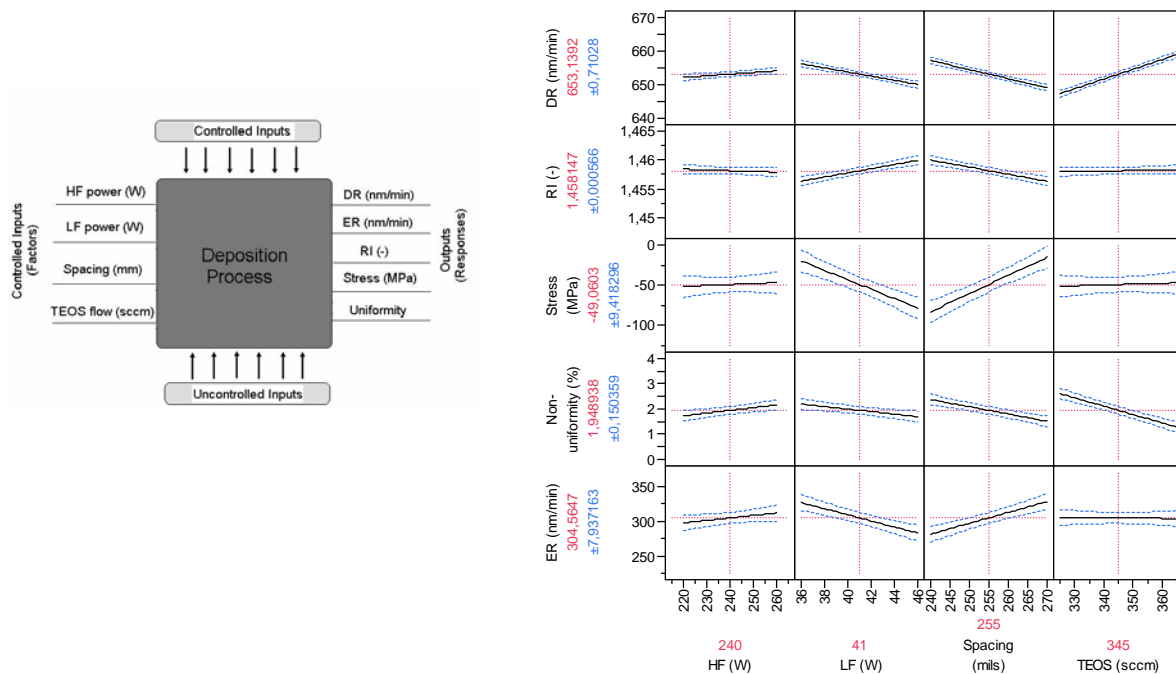
Deposition rate, etch rate, uniformity and refractive index have been measured by ellipsometry (Rudolph FE VII) in 9 points on the wafer. Non-uniformity values were calculated using (standard deviation/mean) x 100. Etch rate has been acquired using the timed buffered oxide etch (1 min., BOE 7:1). The residual stress has been measured by KLA-Tencor FLX-2320. The stress in thin film has been calculated from the radius of curvature of the substrate. In general, for thin film processes, residual stress is the sum of thermal and intrinsic stresses. Due to the difference in thermal -- mechanical properties between the film and substrate, thermal stress is usually unavoidable. Intrinsic stress is generated during the film growth and is strongly dependent on process conditions. Attractive forces within pores can lead to tensile stress, while gas entrapment and its physi- or chemisorption in the inner cavities or at grain boundaries may lead to compressive stress [1]. SiO<sub>x</sub> films prepared by PECVD technique are always in compressive stress but after deposition process the films are annealed and it is highly probable that compressive stress will change to tensile stress. While compressive stresses lead to wrinkling and film delamination, tensile stresses lead to film fracture [3]. The dependence of fracture in thin films on residual stress and annealing process (60 min, 1000 °C, in vacuum and in the air) has been studied by SEM (Scanning electron microscopy) JEOL JSM 6700F. Figure 1 shows fractures in 1µm and 3µm films after annealing process in vacuum.

The influence of power of low frequency, high frequency, inter-electrode spacing and gas flow of TEOS on deposition rate, etch rate, refractive index, uniformity and stress have been studied by DOE (Design of experiment). Full factorial (two levels) Design of experiment has been designed and evaluated in statistical software JMP 7 (Figure 2).

1. R. d'Agostino, P. Favia, Y. Kawai, Advanced Plasma Technology, John Wiley and Sons, 2008, 479s, ISBN 3527405917
2. E.P. van de Ven, I.-W. Connick, A.S. Harrus, Seventh International IEEE Volume , Issue , 12-13 Jun (1990), 194 - 201
3. X. Zhang, K.+ S. Chen, R. Ghodssi, A.A. Ayon, S.M. Spearing, Sensors and Actuators 91(2001), 373-380



**Figure 1.** Fractures in thin SiO<sub>2</sub> film after annealing (60 min, 1000°C, in vacuum). In 1μm film cracks were observed locally on wafer surface while in 3μm film cracks were nearly on the hole wafer surface.



**Figure 2.** Design of experiment and final table. The influence of power of low frequency (LF), high frequency (HF), inter-electrode spacing (Spacing) and gas flow of TEOS (TEOS) on deposition rate (DR), etch rate (ER), refractive index (RI), non-uniformity and stress.